| L Number | Hits | Search Text | DB | Time stamp |
|----------|---------|---|------------|------------------|
| 1 | 2304257 | phase detectors | USPAT; | 2004/03/21 08:15 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| 2 | 3635313 | clock circuits | USPAT; | 2004/03/21 08:16 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| 3 | 4685172 | loop filter circuits | USPAT; | 2004/03/21 08:16 |
| | | | US-PGPUB; | -00 1, 00, 001-0 |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| 4 | 3588870 | oscillator circuits | USPAT; | 2004/03/21 08:16 |
| 7 | 3366670 | OSCIIIALOI CIICUILS | US-PGPUB; | 2007/03/21 00.10 |
| | | | , | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| _ | 700003 | (alana data atawa) and (aland diservite) | IBM_TDB | 2004/02/24 00:40 |
| 5 | 798683 | (phase detectors) and (clock circuits) | USPAT; | 2004/03/21 08:18 |
| | | | US-PGPUB; | |
| · | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| 6 | 3498903 | (clock circuits) and (oscillator circuits) | USPAT; | 2004/03/21 08:29 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| 7 | 770323 | ((phase detectors) and (clock circuits)) and ((clock circuits) and | USPAT; | 2004/03/21 08:37 |
| 1 | | (oscillator circuits)) | US-PGPUB; | |
| [| | | EPO; JPO; | |
| | | | DERWENT; | |
| _ | | | IBM_TDB | |
| 8 | 3509517 | summation circuits | USPAT; | 2004/03/21 08:37 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| 9 | 4500058 | composite error signals | USPAT; | 2004/03/21 08:37 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| 10 | 1858875 | (summation circuits) and (composite error signals) | USPAT; | 2004/03/21 08:40 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| 11 | 594194 | (((phase detectors) and (clock circuits)) and ((clock circuits) and | USPAT; | 2004/03/21 08:45 |
| ļ | | (oscillator circuits))) and ((summation circuits) and (composite | US-PGPUB; | |
| ļ | | error signals)) | EPO; JPO; | |
| İ | | | DERWENT; | |
| | | | IBM_TDB | |
| 12 | 594173 | (loop filter circuits) and ((((phase detectors) and (clock circuits)) | USPAT; | 2004/03/21 08:52 |
| | 33.273 | and ((clock circuits) and (oscillator circuits))) and ((summation | US-PGPUB; | |
| | | circuits) and (composite error signals))) | EPO; JPO; | |
| | | 1 And (composite only digitals/// | , _, _, _, | 1 |
| | | | DERWENT; | |

| 13 | 2024222 | | LICDAT. | 2004/02/24 00 52 |
|------|---------|--|-----------------------|----------------------|
| 13 | 3831222 | clock signals | USPAT; | 2004/03/21 08:53 |
| } | İ | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| 1.4 | 7064570 | | IBM_TDB | 2004/02/24 20 72 |
| 14 | 7064572 | control signals | USPAT; | 2004/03/21 08:53 |
| ļ | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| l | | | IBM_TDB | |
| 15 | 3772038 | (clock signals) and (control signals) | USPAT; | 2004/03/21 08:59 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | i | | IBM_TDB | |
| 16 | 4292781 | pulse generator circuits | USPAT; | 2004/03/21 09:04 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | [|
| 17 | 572901 | ((loop filter circuits) and ((((phase detectors) and (clock circuits)) | USPAT; | 2004/03/21 09:09 |
| | | and ((clock circuits) and (oscillator circuits))) and ((summation | US-PGPUB; | |
| | İ | circuits) and (composite error signals)))) and ((clock signals) and | EPO; JPO; | |
| | | (control signals)) | DERWENT; | |
| | | | IBM_TDB | |
| 18 | 572881 | (pulse generator circuits) and (((loop filter circuits) and ((((phase | USPAT; | 2004/03/21 09:20 |
| | | detectors) and (clock circuits)) and ((clock circuits) and (oscillator | US-PGPUB; | 200 ,, 00, 22 05 .20 |
| | | circuits))) and ((summation circuits) and (composite error | EPO; JPO; | |
| | 1 | signals)))) and ((clock signals) and (control signals))) | DERWENT; | |
| | | (control digitally) | IBM_TDB | |
| 19 | 225293 | synchroniz\$3 and (phase\$1 with clock signal\$1) | USPAT; | 2004/03/21 09:20 |
| | | Synamonized and (phaselp with dock signaler) | US-PGPUB; | 200 1/03/21 03.20 |
| | | | EPO; JPO; | |
| | | _ | DERWENT; | |
| | | | IBM_TDB | |
| 20 | 95328 | ((pulse generator circuits) and (((loop filter circuits) and ((((phase | USPAT; | 2004/03/21 09:23 |
| | 35020 | detectors) and (clock circuits)) and ((clock circuits) and (oscillator | US-PGPUB; | 2004/03/21 03.23 |
| | | circuits))) and ((summation circuits) and (composite error | EPO; JPO; | |
| | | signals)))) and ((clock signals) and (control signals)))) and | DERWENT; | |
| | | (synchroniz\$3 and (phase\$1 with clock signal\$1)) | IBM_TDB | |
| 21 | 3373415 | | USPAT; | 2004/03/21 09:24 |
| | 3373713 | Single integrated emp | US-PGPUB; | 2007/03/21 03.24 |
| | | | | |
| | | | EPO; JPO; DERWENT; | |
| | | | | |
| 22 | 66885 | (((pulse generator circuits) and (((loop filter circuits) and | IBM_TDB | 2004/03/24 00:26 |
| " | 00003 | ((((phase detectors) and (clock circuits)) and ((clock circuits) and | USPAT; | 2004/03/21 09:26 |
| 1 | | | US-PGPUB; | |
| 1 | | (oscillator circuits))) and ((summation circuits) and (composite | EPO; JPO; | |
| | | error signals)))) and ((clock signals) and (control signals)))) and | DERWENT; | |
| | | (synchroniz\$3 and (phase\$1 with clock signal\$1))) and (single | IBM_TDB | |
| 22 | 5350 | integrated chip) | | 0004/00/01 00 00 |
| 23 | 5250 | 714/789.ccls. or 714/700.ccls. or 714/699.ccls. or 714/7738.ccls. | USPAT; | 2004/03/21 09:27 |
| | | or 714/744.ccls. or 714/814.ccls. or 714/815.ccls. or 327/?.ccls. or | US-PGPUB; | |
| • | | 713/?.ccls. | EPO; JPO; | |
| 1 | | | DERWENT; | |
| 1 34 | | , , , , , , , , , , , , , , , , , , , | IBM_TDB | |
| 24 | 512 | ((((pulse generator circuits) and (((loop filter circuits) and | USPAT; | 2004/03/21 09:28 |
| 1 | | ((((phase detectors) and (clock circuits)) and ((clock circuits) and | US-PGPUB; | |
| | | (oscillator circuits))) and ((summation circuits) and (composite | EPO; JPO; | |
| | | error signals)))) and ((clock signals) and (control signals)))) and | DERWENT; | |
| | | (synchroniz\$3 and (phase\$1 with clock signal\$1))) and (single | IBM_TDB | |
| |] | integrated chip)) and (714/789.ccls. or 714/700.ccls. or | | |
| | 1 | 714/699.ccls. or 714/7738.ccls. or 714/744.ccls. or 714/814.ccls. | | |
| | | or 714/815.ccls. or 327/?.ccls. or 713/?.ccls.) | | |

| 25 | 98 | ("6066969" | USPAT; | 2004/03/21 09:2 |
|----|----|------------------------|-----------|-----------------|
| | | "6088255" | US-PGPUB; | |
| | | "4816700" | EPO; JPO; | |
| | | "5870595" | DERWENT; | |
| | | "6041073" | IBM_TDB | |
| | | "4475085" | | |
| | | "5212410" | | |
| | | "5630102" | | |
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| | | "5459762" | | |
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| 26 | 2 | (((((pulse generator circuits) and (((loop filter circuits) and ((((phase detectors) and (clock circuits)) and ((clock circuits) and | USPAT; US-PGPUB; | 2004/03/21 09:29 |
|----|---|--|---------------------|------------------|
| | | (oscillator circuits))) and ((summation circuits) and (composite | EPO; JPO; | • |
| | | error signals)))) and ((clock signals) and (control signals)))) and | DERWENT; | |
| | | (synchroniz\$3 and (phase\$1 with clock signal\$1))) and (single | IBM_TDB | |
| | i | integrated chip)) and (714/789.ccls. or 714/700.ccls. or | _ | |
| | † | 714/699.ccls. or 714/7738.ccls. or 714/744.ccls. or 714/814.ccls. | | |
| | : | or 714/815.ccls. or 327/?.ccls. or 713/?.ccls.)) and (("6066969" | | |
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| | | "5630102" | | |
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| | | "4905006" | | |
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| | 1 | "6078225" "6004076" | | |
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| | | "4361897" "437F099" | | |
| | | "4375088" "4376369" | | |
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